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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Attention: Examiner R. Pompey  
Art Unit: 2812

Re: M. YOSHIDA, et al. – Application No.: 09/416,959  
Our Ref. No.: 501.35437CV2

**SUBMISSION OF DOCUMENT**RECEIVED  
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DIPE/JCWS

Sir:

Applicants hereby submit the attached "Amendment After Final Rejection" (9 pgs), Petition for Extension of Time (1) and a Credit Card Payment Form (1 pg) in the above-identified application.

**CERTIFICATE OF TRANSMISSION:**

I hereby certify that the attached "Amendment After Final Rejection" (9 pgs), Petition for Extension of Time (1) and a Credit Card Payment Form (1 pg) are being formally filed via the USPTO Central Fax No. 703-872-9306 on May 14, 2004.

  
Deborah L. Therrien

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501.35437CV2  
Response Under 37 CFR § 1.116  
Expedited Procedure  
Group No. 2812

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Applicants: M. YOSHIDA et al.  
Application No.: 09/416,959  
Filed: October 13, 1999  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
PROCESS FOR MANUFACTURING THE SAME  
Art Unit: 2812  
Examiner: R. Pompey

**AMENDMENT AFTER FINAL REJECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

May 14, 2004

Sir:

In accordance with the provisions of 37 CFR § 1.116, and based upon discussions conducted with Examiner Pompey during a telephone interview on May 11, 2004, please amend the above-identified application as listed below and as set forth on the following pages, noting that a Petition for Extension of Time to permit time for entry of this Amendment is being filed herewith:

**Amendments to the Claims and**

**Remarks are included following the amendments.**